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F-068

**Analog low-pass filter with double sampling**

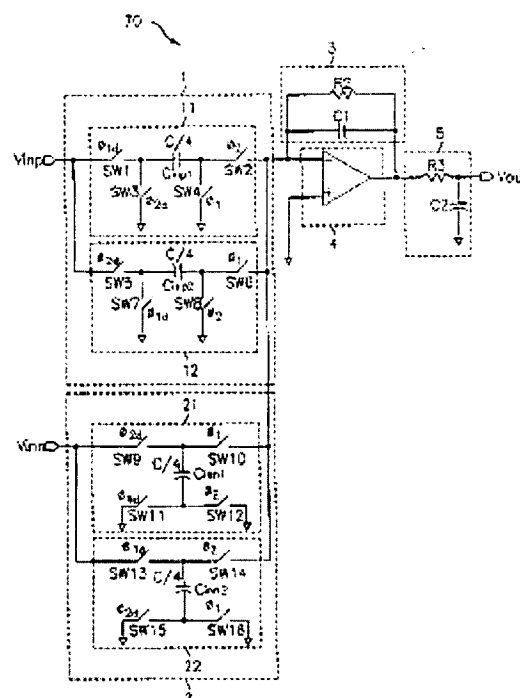
**Patent number:** FR2765417  
**Publication date:** 1998-12-31  
**Inventor:** SHIN YUN TAE  
**Applicant:** HYUNDAI ELECTRONICS IND (KR)  
**Classification:**  
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**Abstract of FR2765417**

The filter (70) includes two terminals (1,2) which receive differential analog signals ( $V_{inP}$ ,  $V_{inN}$ ) which charge / discharge four capacitors ( $C_{inn1}$ ,  $C_{inn2}$ ,  $C_{inp1}$ ,  $C_{inp2}$ ). A differential amplifier (4) is connected to the common terminal of the capacitors. A filtering module (5) is connected between the amplifier asynchronous output and the filter output terminal. Switches (SW1-SW8) control the charge / discharge sequence of the capacitors. These switches are controlled by a clock signal.



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